



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re U.S. Patent Application of**

**SHIMAMOTO *et al.***

**Application Number: 10/020,314**

**Filed: December 18, 2001**

**For: FABRICATING METHOD OF  
SEMICONDUCTOR INTEGRATED CIRCUITS**

**Attorney Docket No. HITA.0143**

**Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**

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) Art Unit 2823  
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) Examiner  
) Nguyen, Khiem D.  
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**RESPONSE AND AMENDMENT UNDER 37 C.F.R. §1.111**

Sir:

This is in response to the office action dated October 31, 2006, in the above identified application, the period for response is set to expire on March 30, 2007, with a two-month extension filed herewith. Please amend the above-referenced application as follows: